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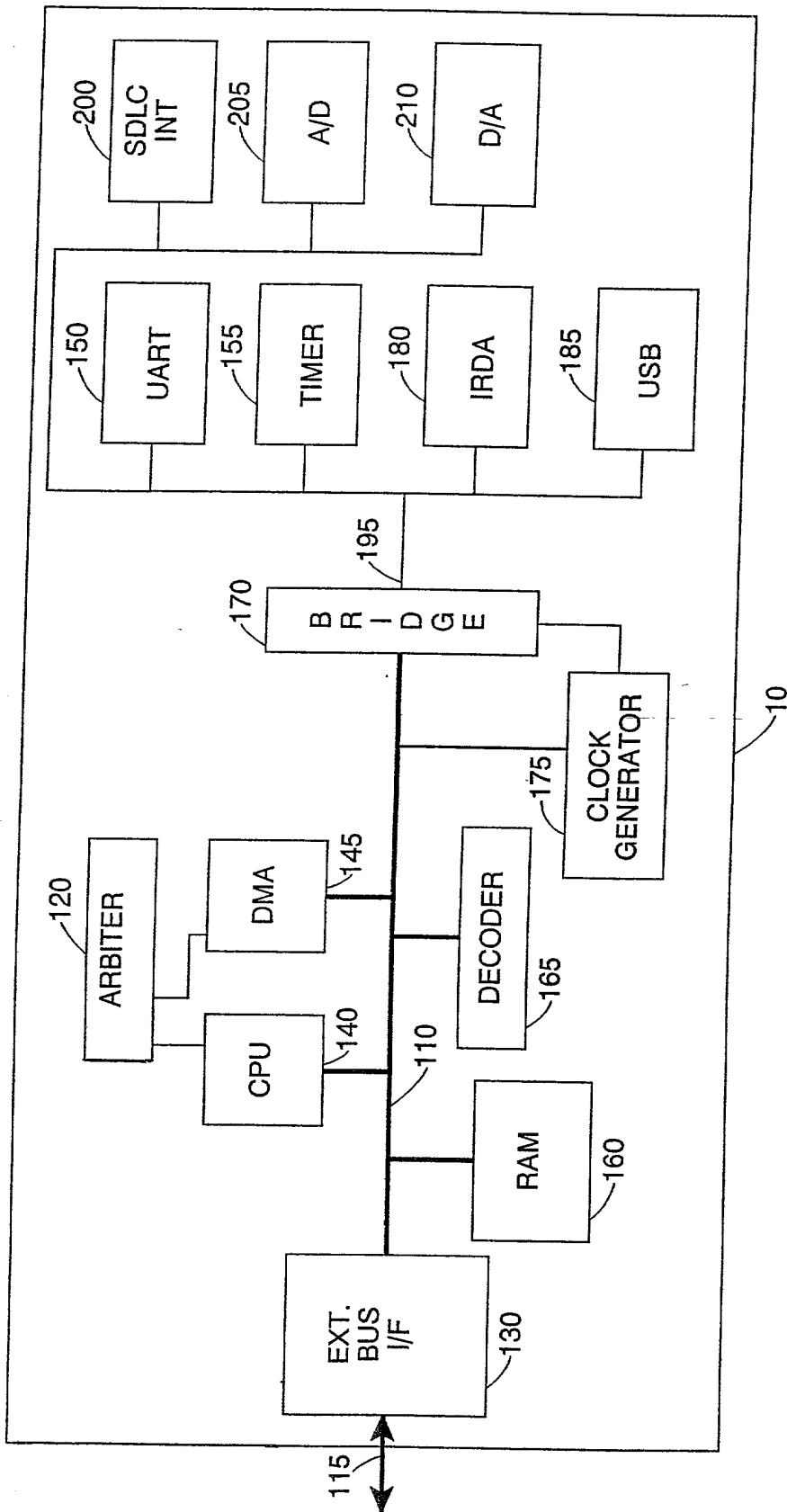
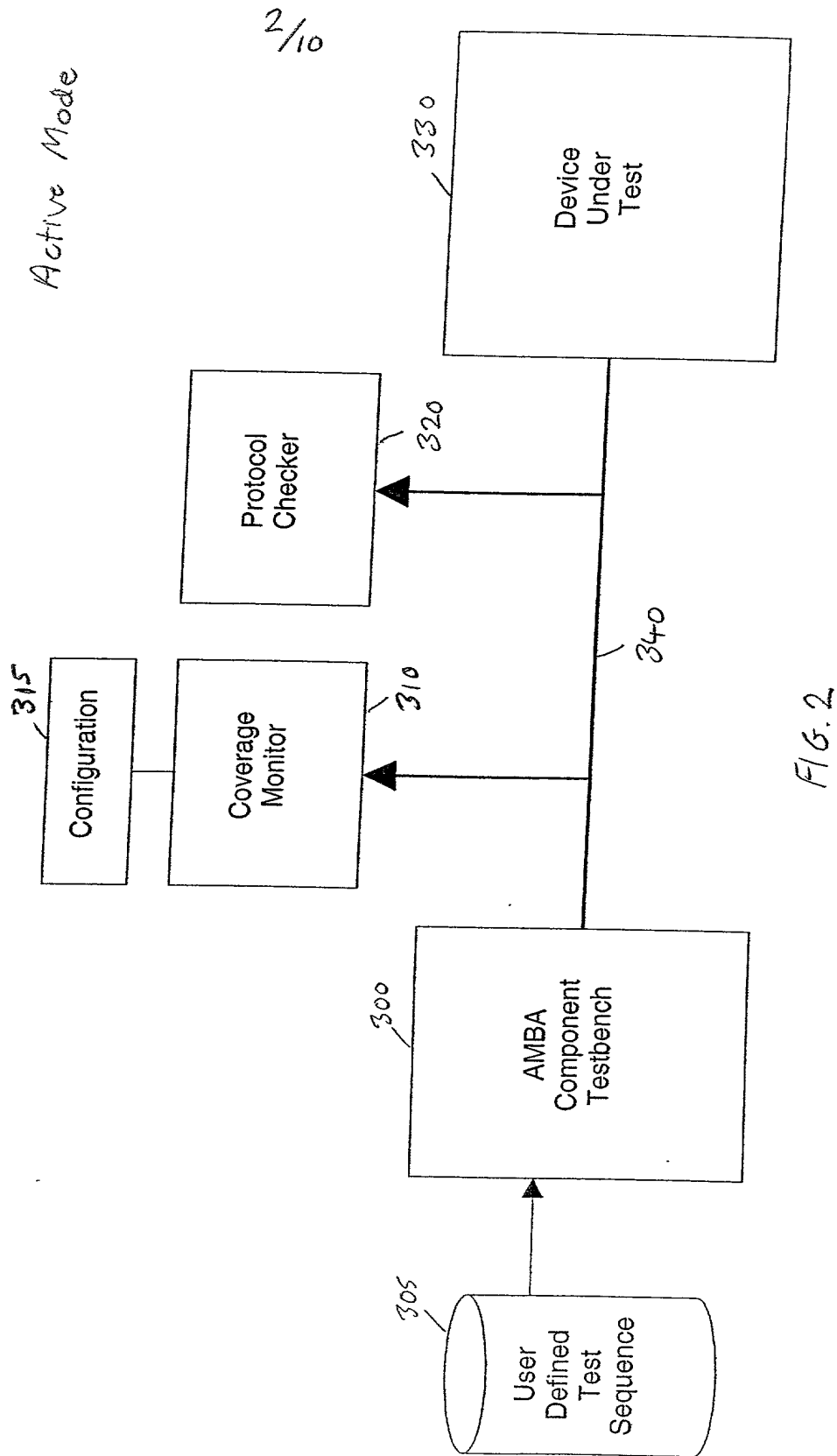


FIG. 1



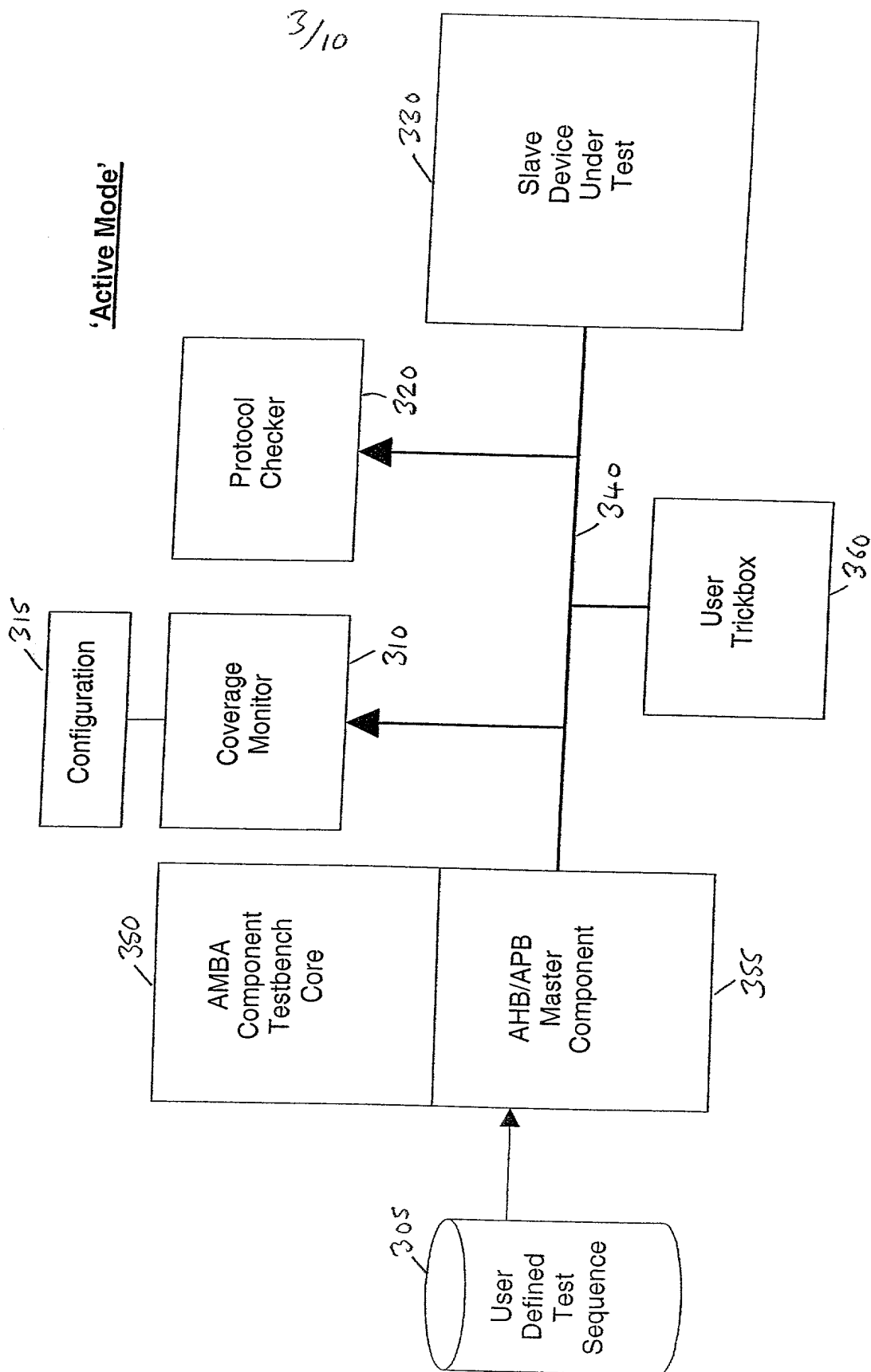


Fig. 3

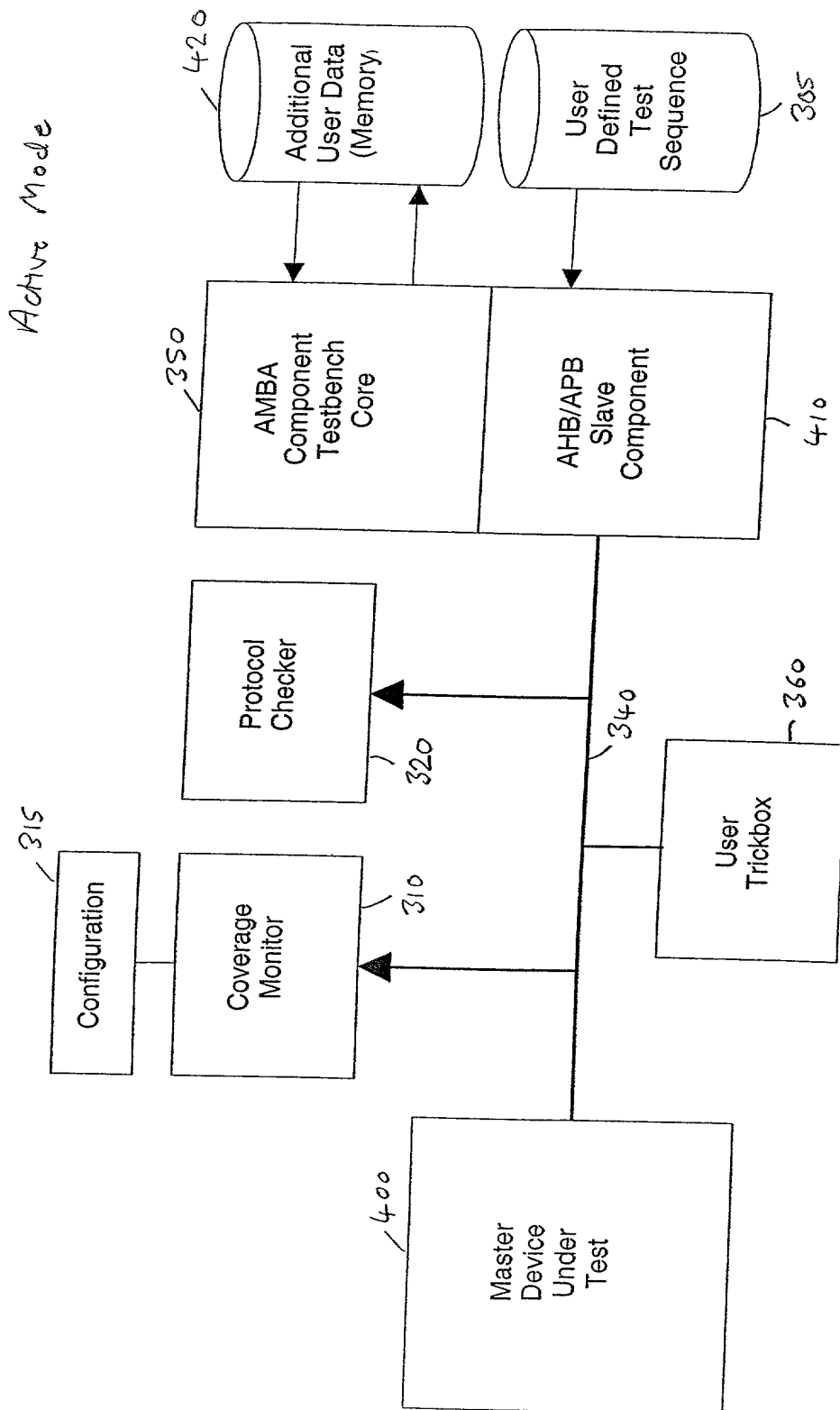
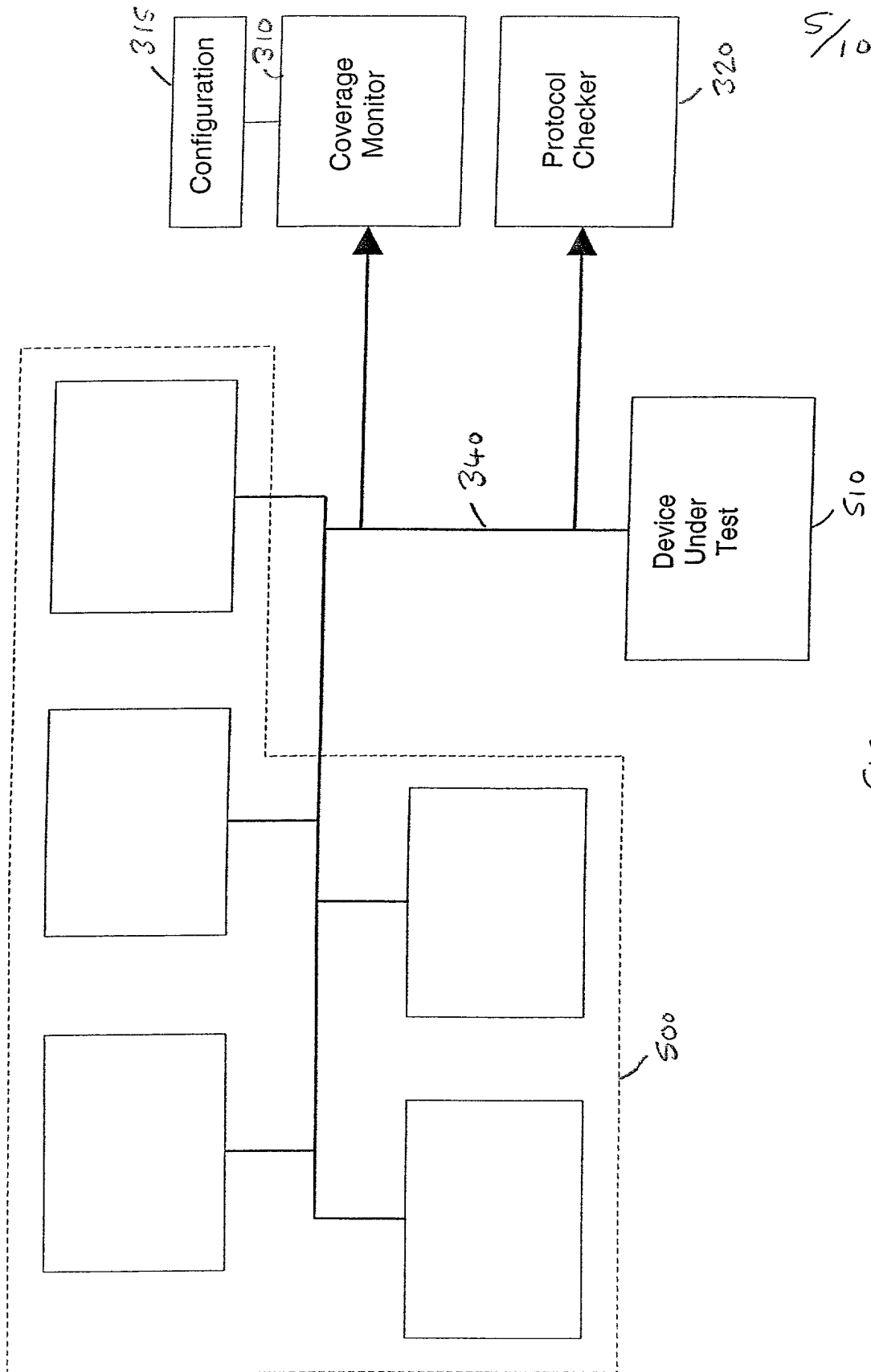


FIG. 4

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'Passive Mode'

System Simulation Environment



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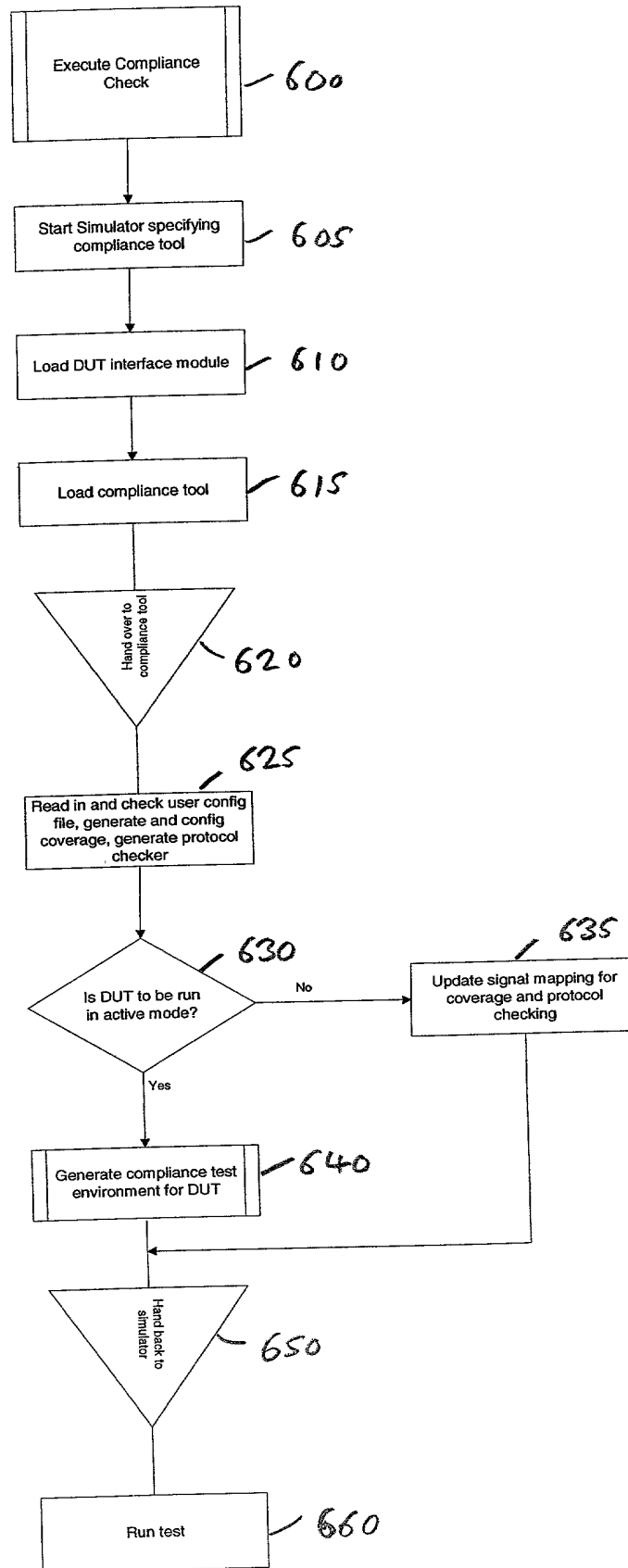


FIG. 6

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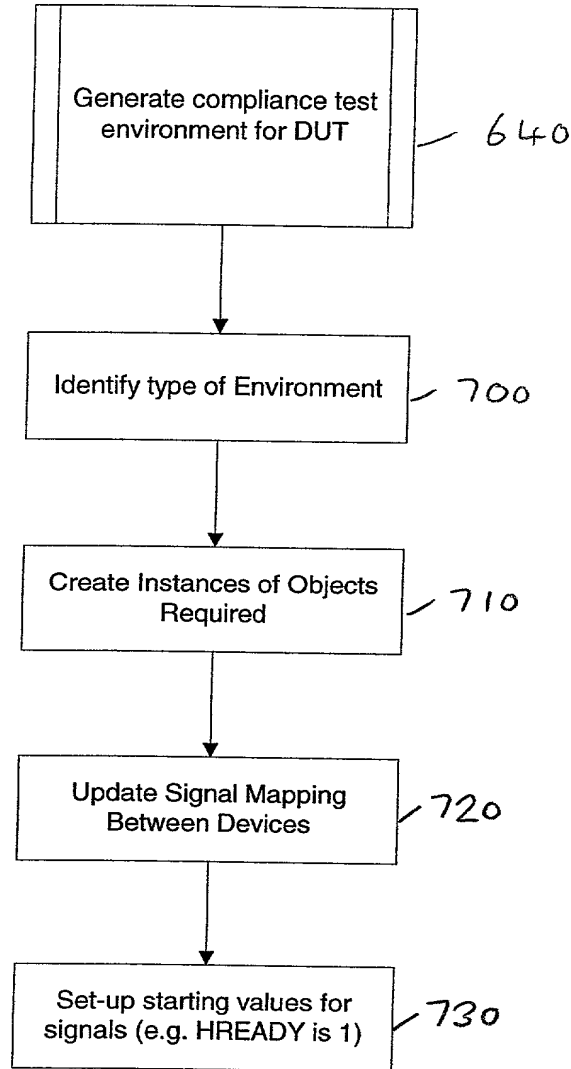


FIG. 7

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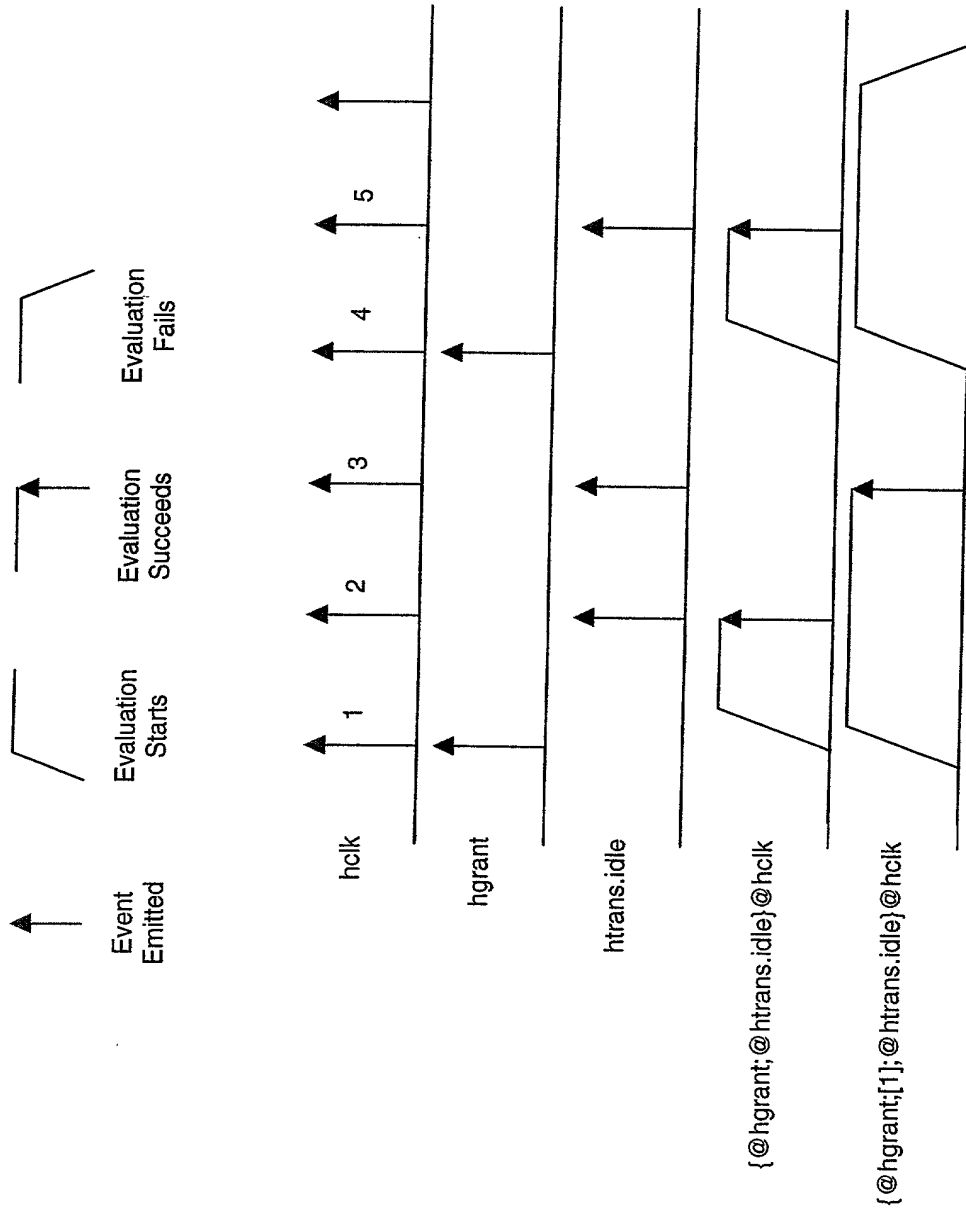


FIG. 8



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FIG. 9A

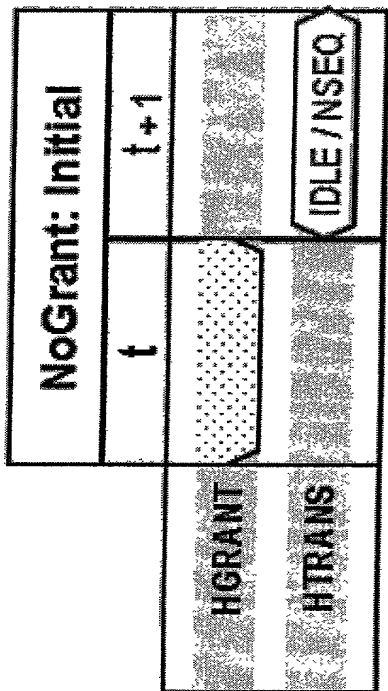
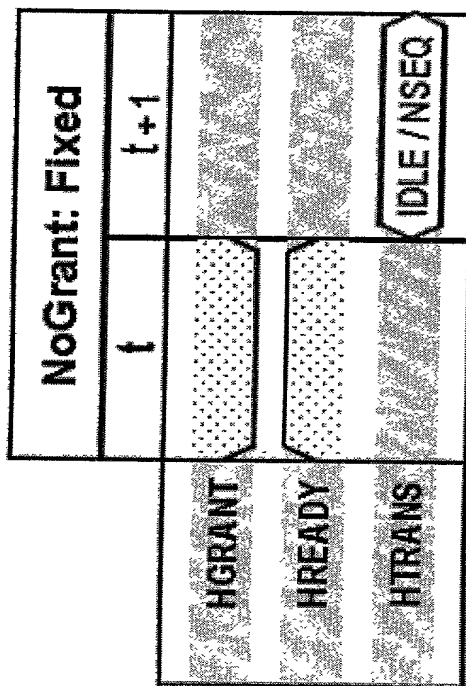


FIG. 9B



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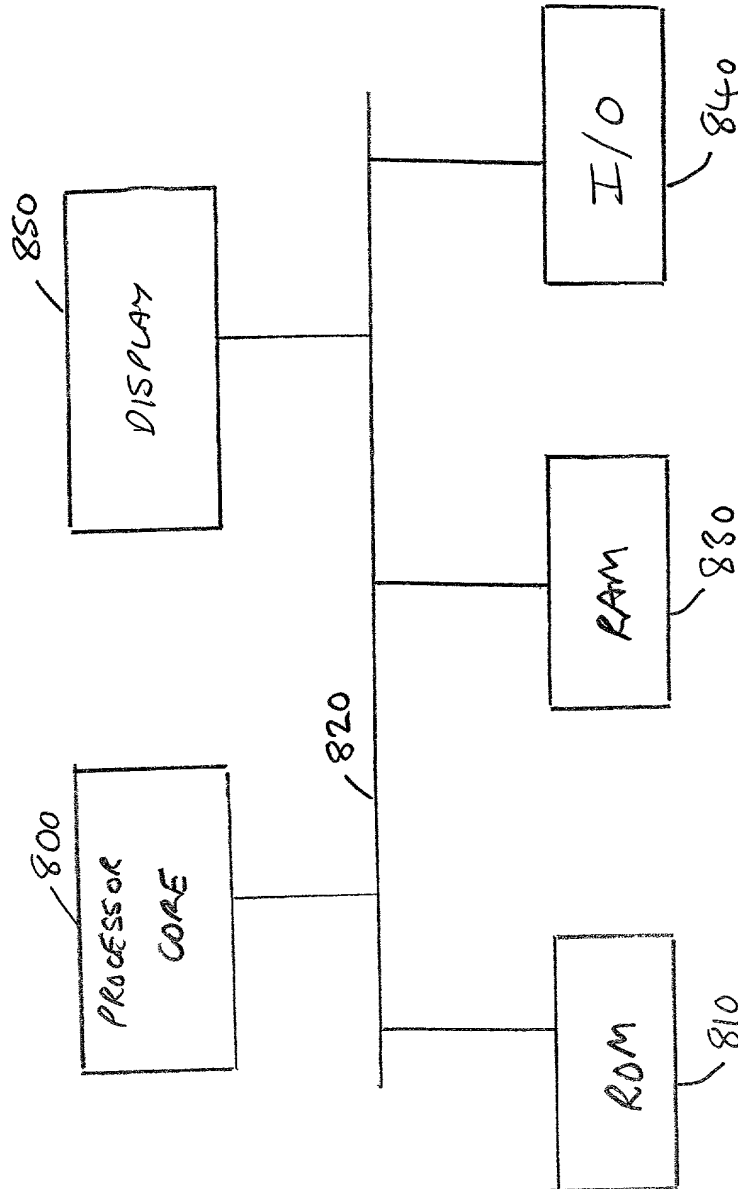


FIG. 10